



S/N 10/790,510

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Leonard Forbes et al.	Examiner:	Unknown
Serial No.:	10/790,510	Group Art Unit:	2823
Filed:	March 1, 2004	Docket:	303.689US2
Title:	STATIC PASS TRANSISTOR LOGIC WITH TRANSISTORS WITH MULTIPLE VERTICAL GATES		

AMENDMENT UNDER 37 C.F.R. § 1.312

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Notice of Allowance and Notice of Allowability mailed August 10, 2005, please amend as follows: